



- ★ Super Low Gate Charge
- ★ 100% EAS Guaranteed
- ★ Green Device Available
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology

Description

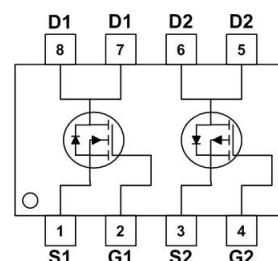
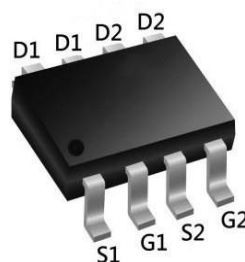
The UD10G06S is the high cell density trenched N-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The UD10G06S meet the RoHS and Green Product requirement 100% EAS guaranteed with full function reliability approved.

Product Summary

BVDSS	RDSON	ID
60V	32mΩ	10A
-60V	51mΩ	-10A

SOP8 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating		Units
		N-Channel	P-Channel	
V_{DS}	Drain-Source Voltage	60	-60	V
V_{GS}	Gate-Source Voltage	± 20	± 20	V
$I_D@T_A=25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V^1$	6.0	-10	A
$I_D@T_A=70^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V^1$	3.8	-5.5	A
I_{DM}	Pulsed Drain Current ²	30	-22	A
EAS	Single Pulse Avalanche Energy ³	22.5	28.8	mJ
I_{AS}	Avalanche Current	22.6	-26.6	A
$P_D@T_A=25^\circ\text{C}$	Total Power Dissipation ⁴	2.3	4	W
T_{STG}	Storage Temperature Range	-55 to 150	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	-55 to 150	$^\circ\text{C}$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	---	---	$^\circ\text{C/W}$
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	4	$^\circ\text{C/W}$

Electrical Characteristics (T_J=25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	60	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =60V, V _{GS} =0V,	-	-	1.0	μA
I _{GSS}	Gate to Body Leakage Current	V _{DS} =0V, V _{GS} =±20V	-	-	±100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	1	1.6	2.5	V
R _{DS(on)}	Static Drain-Source on-Resistance note3	V _{GS} =10V, I _D =15A	-	32	40	mΩ
		V _{GS} =4.5V, I _D =10A	-	39	50	
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =25V, V _{GS} =0V, f=1.0MHz	-	825	-	pF
C _{oss}	Output Capacitance		-	49	-	pF
C _{rss}	Reverse Transfer Capacitance		-	41	-	pF
Q _g	Total Gate Charge	V _{DS} =30V, I _D =4.5A, V _{GS} =10V	-	14	-	nC
Q _{gs}	Gate-Source Charge		-	2.9	-	nC
Q _{gd}	Gate-Drain(“Miller”) Charge		-	5.2	-	nC
Switching Characteristics						
t _{d(on)}	Turn-on Delay Time	V _{DS} =30V,I _D =2A, R _L =6.7Ω,R _G =3Ω, V _{GS} =10V	-	5	-	ns
t _r	Turn-on Rise Time		-	2.6	-	ns
t _{d(off)}	Turn-off Delay Time		-	16.1	-	ns
t _f	Turn-off Fall Time		-	2.3	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain to Source Diode Forward Current		-	-	6	A
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	30	A
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} =0V, I _S =15A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	T _J =25℃, I _F =15A, dI/dt=100A/μs	-	35	-	ns
Qrr	Body Diode Reverse Recovery Charge		-	53	-	nC

Notes: 1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition : T_J=25°C, V_{DD}=30V, V_G=10V, L=0.5mH, R_G=25Ω, I_{AS}=6.1A

3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%

P-Channel Electrical Characteristics ($T_J=25\text{ }^{\circ}\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V$, $I_D=-250\mu A$	-60	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to 25°C , $I_D=-1\text{mA}$	---	-0.03	---	V/ $^{\circ}\text{C}$
$R_{DS(ON)}$	Static Drain-Source On-Resistance ²	$V_{GS}=-10V$, $I_D=-1A$	---	51	70	m Ω
		$V_{GS}=-4.5V$, $I_D=-1A$	---	65	81	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=-250\mu A$	-1.2	-1.7	-2.2	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	---	---	mV/ $^{\circ}\text{C}$
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=-60V$, $V_{GS}=0V$, $T_J=25^{\circ}\text{C}$	---	---	1	μA
		$V_{DS}=-60V$, $V_{GS}=0V$, $T_J=55^{\circ}\text{C}$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V$, $V_{DS}=0V$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{DS}=-10V$, $I_D=-5A$	---	13	---	S
R_g	Gate Resistance	$V_{DS}=0V$, $V_{GS}=0V$, $f=1\text{MHz}$	---	13	---	Ω
Q_g	Total Gate Charge (-4.5V)	$V_{DS}=-30V$, $V_{GS}=-10V$, $I_D=-5A$	---	21	---	nC
Q_{gs}	Gate-Source Charge		---	2.9	---	
Q_{gd}	Gate-Drain Charge		---	5	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=-30V$, $V_{GS}=-10V$, $R_G=3.3\Omega$, $I_D=-5A$	---	7.8	---	ns
T_r	Rise Time		---	4.5	---	
$T_{d(off)}$	Turn-Off Delay Time		---	39	---	
T_f	Fall Time		---	7.8	---	
C_{iss}	Input Capacitance	$V_{DS}=-15V$, $V_{GS}=0V$, $f=1\text{MHz}$	---	1170	---	pF
C_{oss}	Output Capacitance		---	65	---	
C_{rss}	Reverse Transfer Capacitance		---	52	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current ^{1,5}	$V_G=V_D=0V$, Force Current	---	---	-5.5	A
I_{SM}	Pulsed Source Current ^{2,5}		---	---	-15	A
V_{SD}	Diode Forward Voltage ²	$V_{GS}=0V$, $I_S=-1A$, $T_J=25^{\circ}\text{C}$	---	---	-1.2	V

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is $V_{DD}=-25V$, $L=0.4\text{mH}$, $I_{AS}=-12A$.
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Performance Characteristics

Figure 1: Output Characteristics

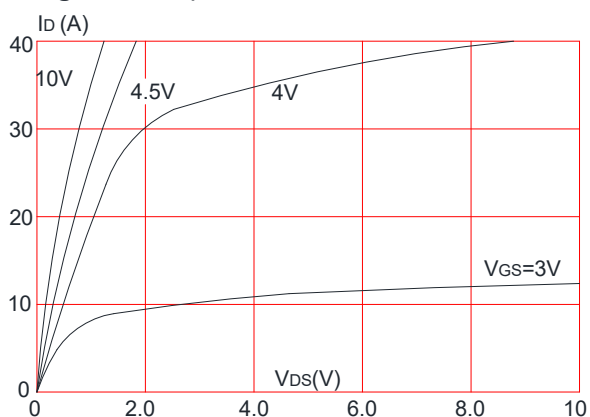


Figure 2: Typical Transfer Characteristics

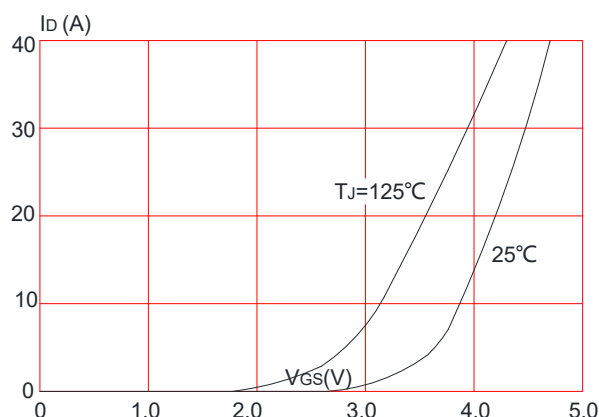


Figure 3: On-resistance vs. Drain Current

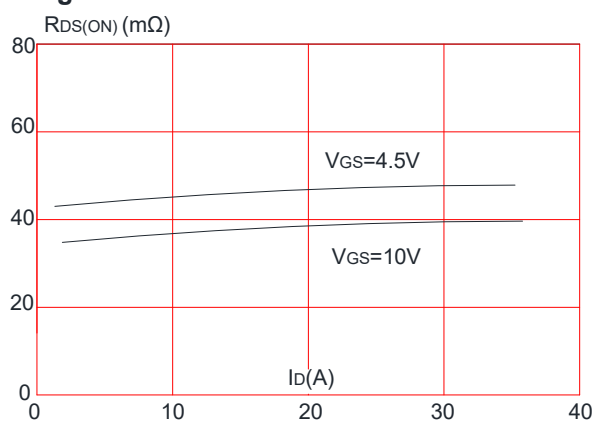


Figure 4: Body Diode Characteristics

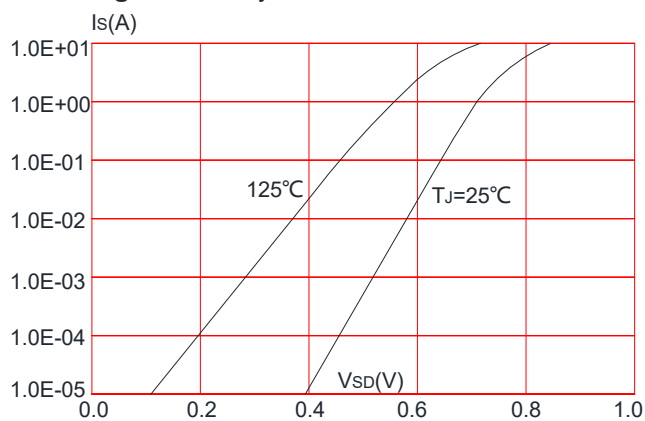


Figure 5: Gate Charge Characteristics

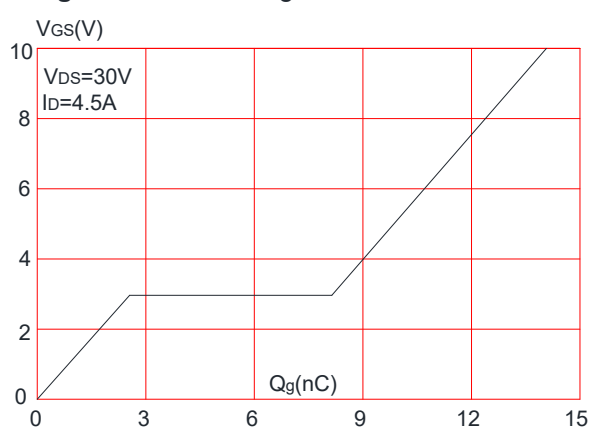
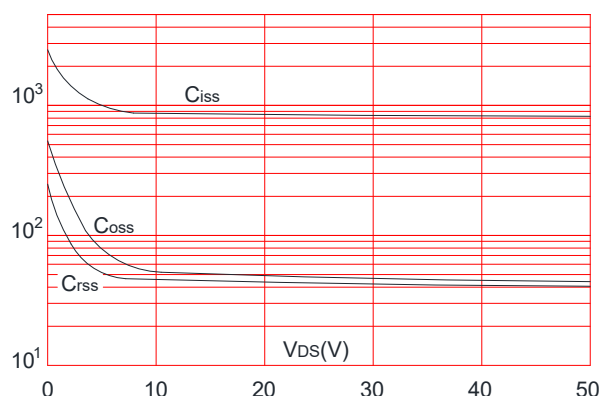


Figure 6: Capacitance Characteristics



N-Ch and P-Ch Fast Switching MOSFETs

Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

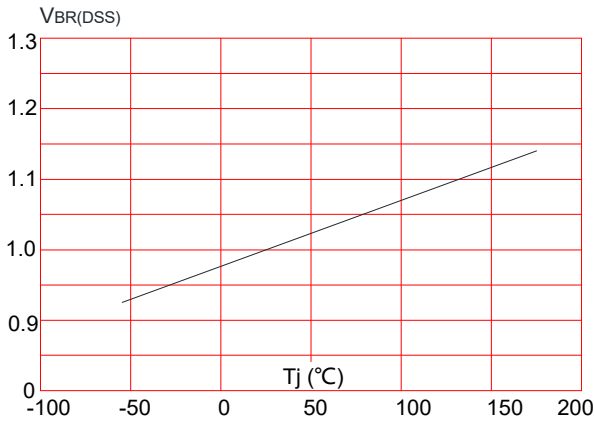


Figure 8: Normalized on Resistance vs. Junction Temperature

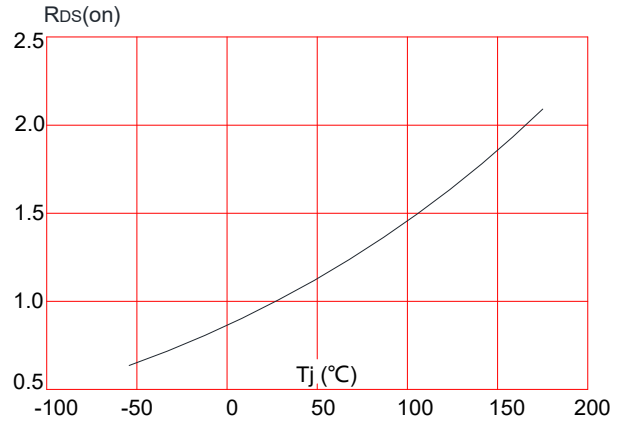


Figure 9: Maximum Safe Operating Area

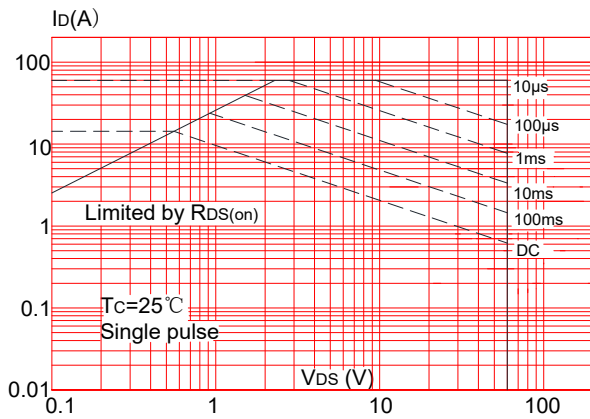


Figure 10: Maximum Continuous Drain Current vs. Case Temp

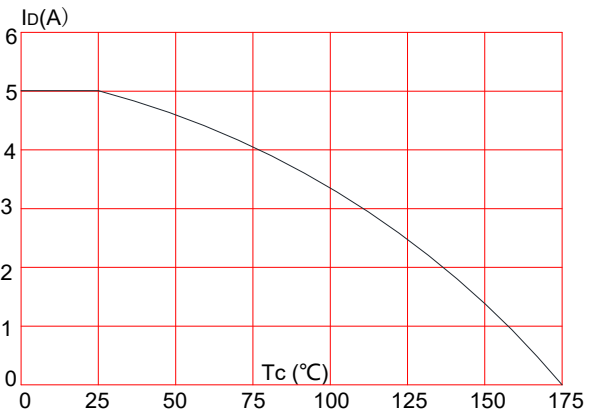
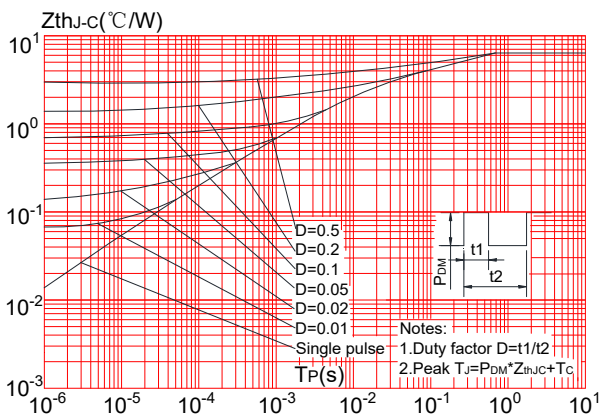


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Case



P-Channel Typical Characteristics

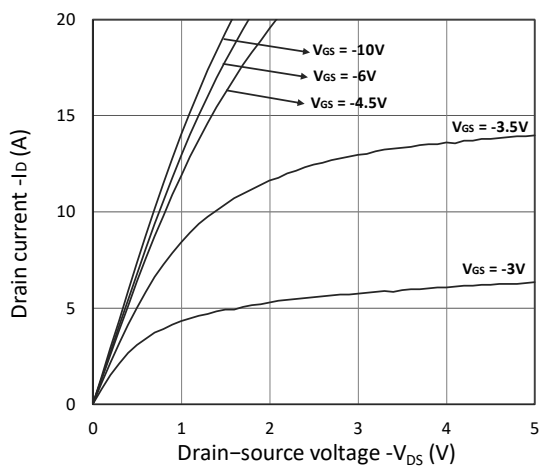


Figure 1. Output Characteristics

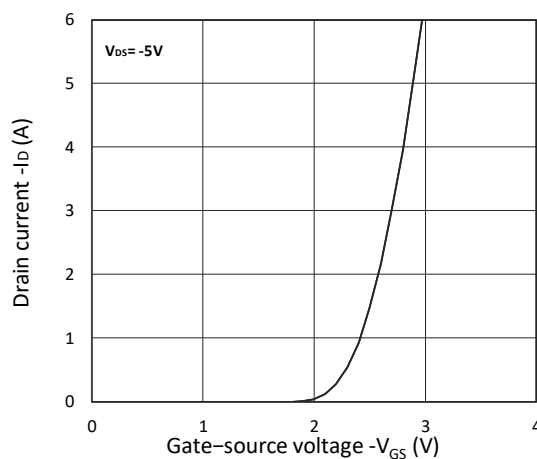


Figure 2. Transfer Characteristics

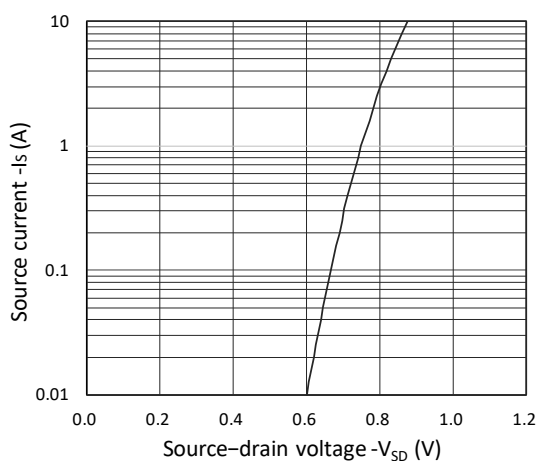


Figure 3. Forward Characteristics of Reverse

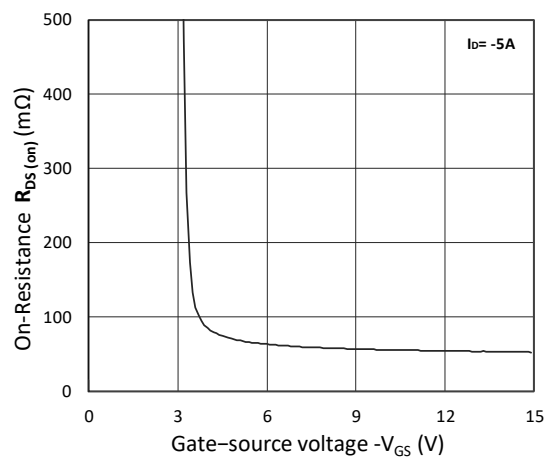


Figure 4. $R_{DS(on)}$ vs. V_{GS}

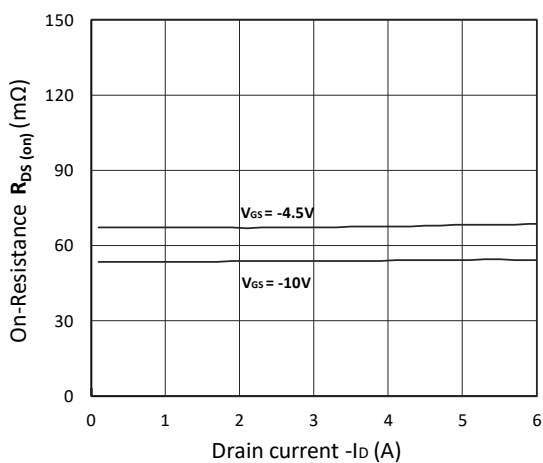


Figure 5. $R_{DS(on)}$ vs. I_D

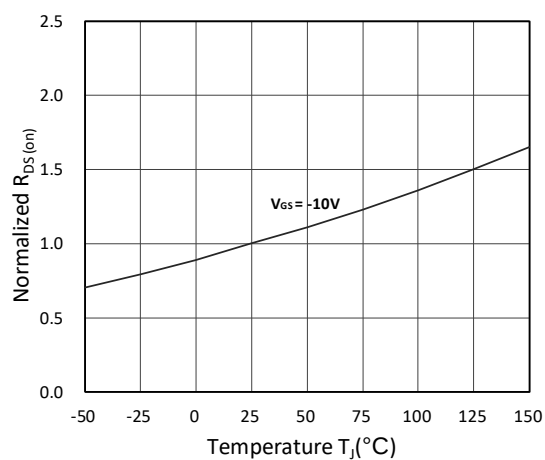


Figure 6. Normalized $R_{DS(on)}$ vs. Temperature

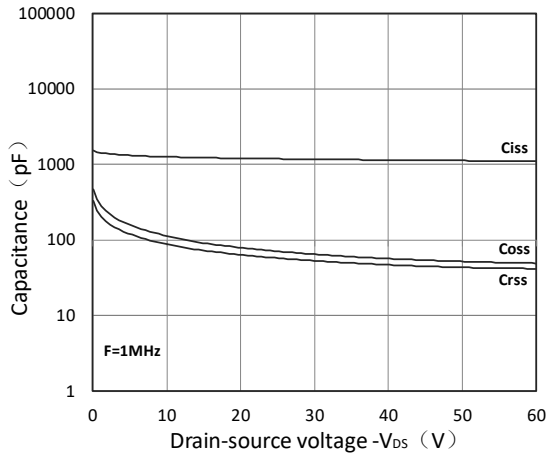


Figure 7. Capacitance Characteristics

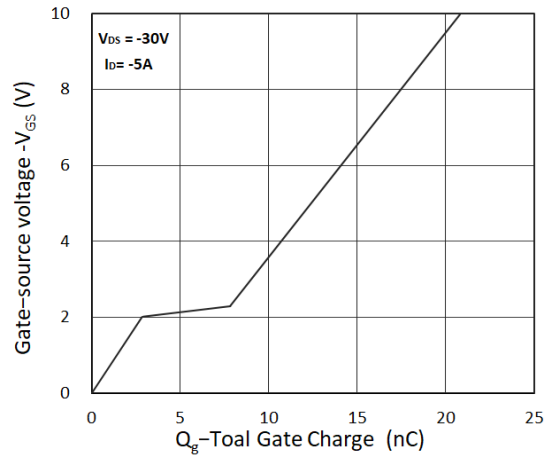


Figure 8. Gate Charge Characteristics

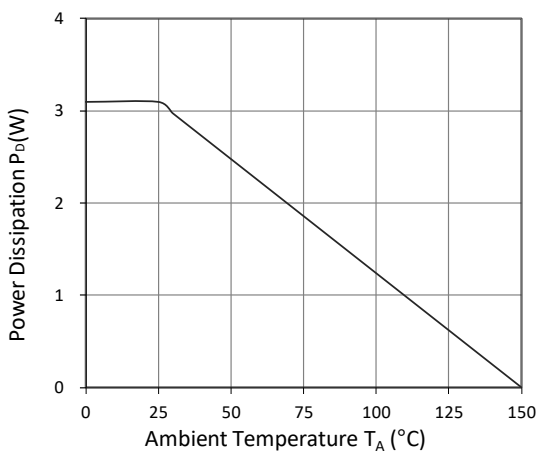


Figure 9. Power Dissipation

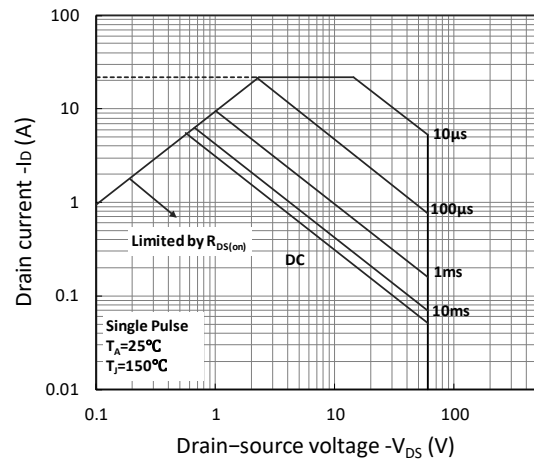


Figure 10. Safe Operating Area

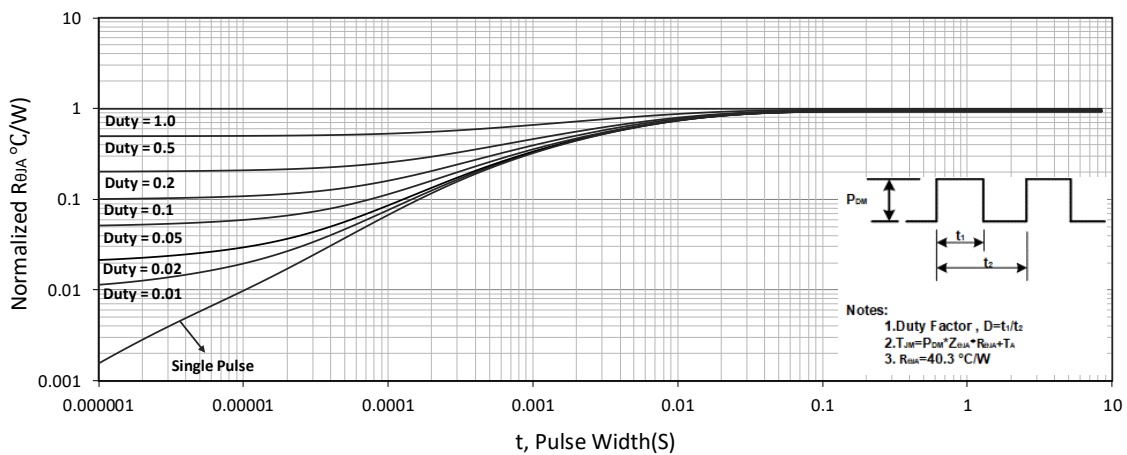
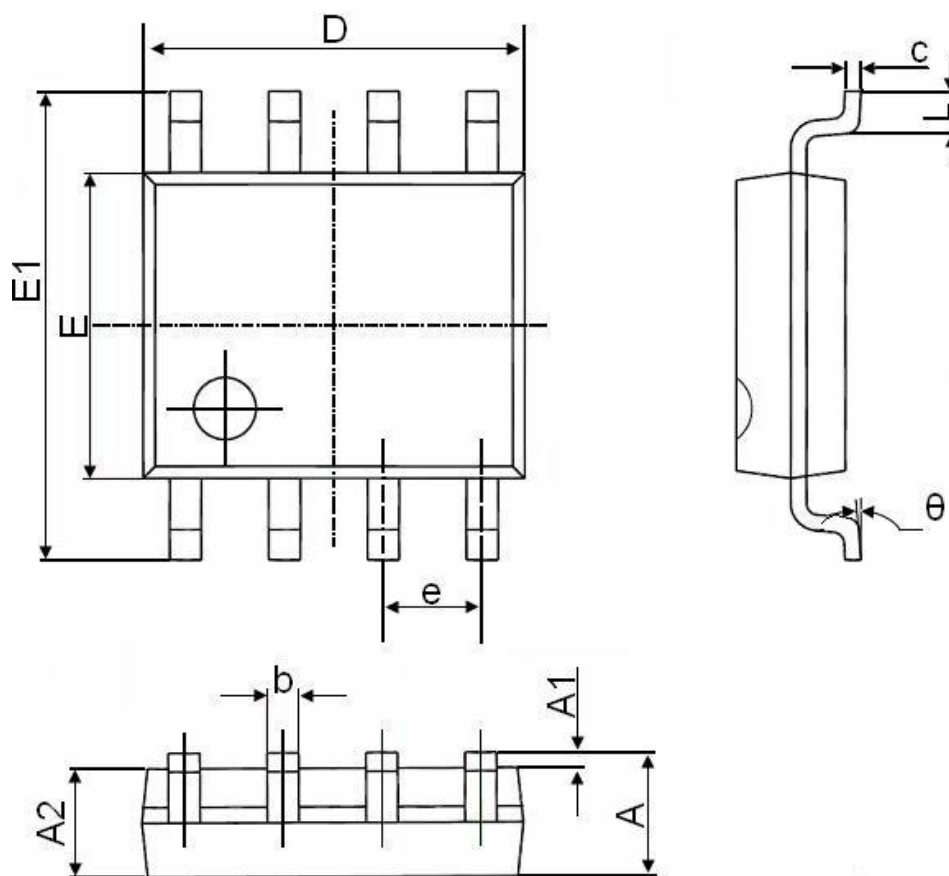


Figure 11. Normalized Maximum Transient Thermal Impedance

SOP-8 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°